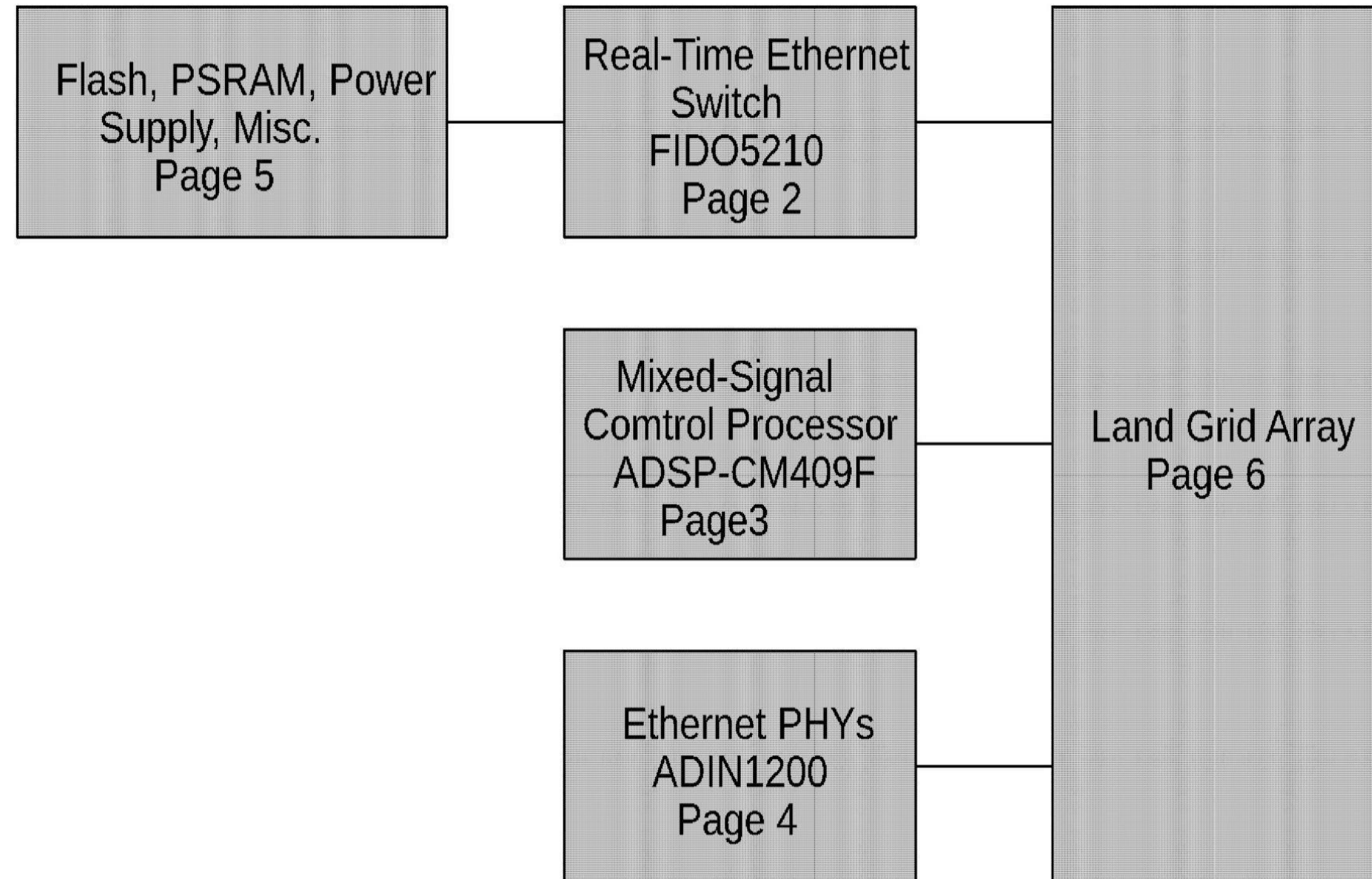


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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	10FEB22	C. CLERY

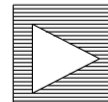
ADIN2299

NEXT GENERATION PADID PLATFORM - CSP-BGA MODULE



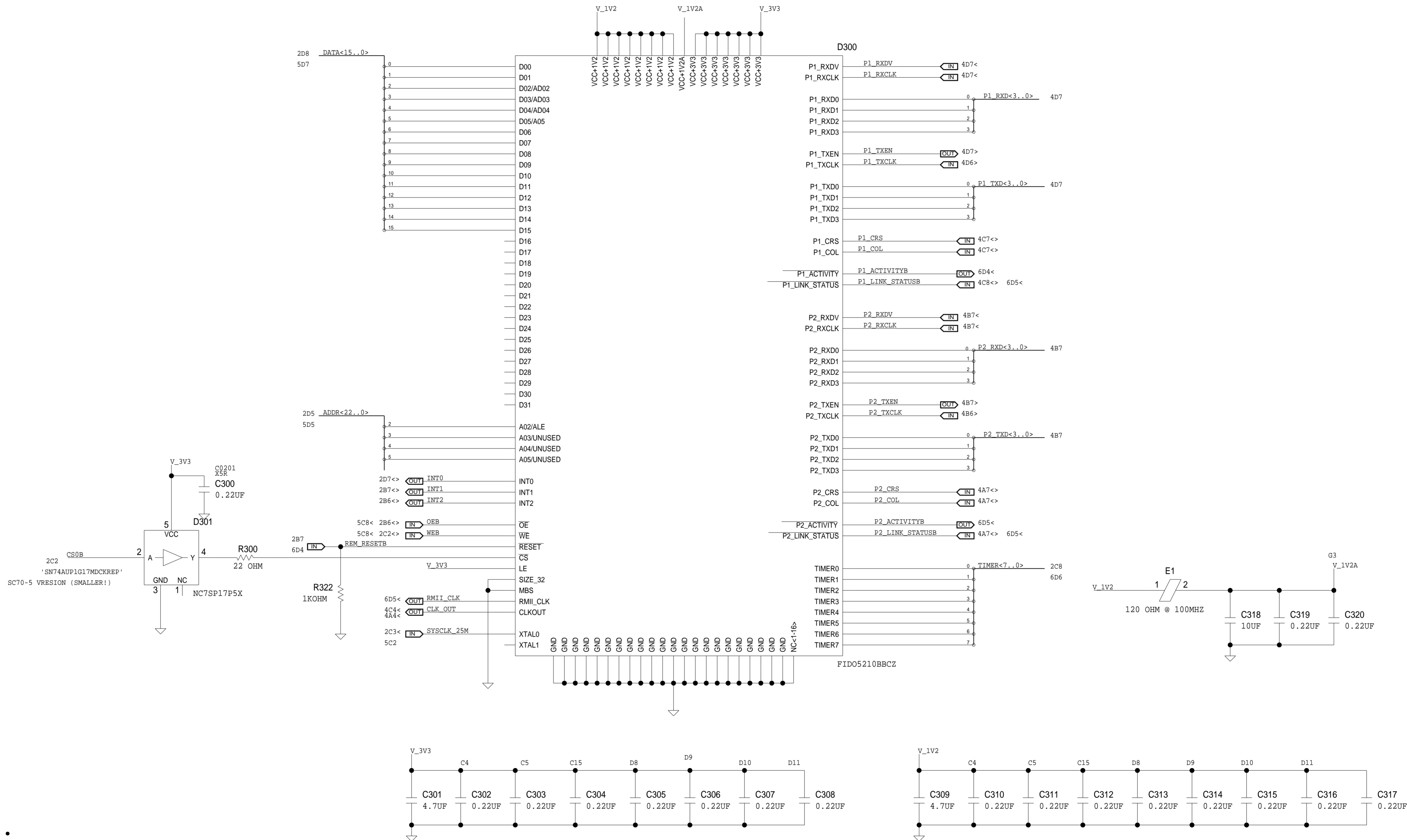
CAUTION:

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QUALIFIED FOR USE WITH THE INTENDED INDUSTRIAL PROTOCOLS.
SUBSTITUTION OR OMISSION MAY RESULT IN ISSUES
AFFECTING INDUSTRIAL PROTOCOL CERTIFICATION

DSM J. ZAGAMI	DATE 10FEB22	SCHEMATIC				 ANALOG DEVICES			
PACKAGING_ENGINEER C. CLERY	10FEB22								
DESIGN_ENGINEER R. BERRGER	10FEB22	HW TYPE : Package Design							
LAYOUT_ENGINEER J. ZAGAMI	10FEB22	Product(s): ADIN2299							
PKG_COMPONENT_ENGINEER N. NAGASANGAN	10FEB22	<PRODUCT_1>							
		-							
		-							
		-							
		-							
		-							
		TECHNOLOGY TEMPLATE FILE 041927_b		CONSTRAINT FILE TBD		DRAWING NO. 02_058603		REV. A	
		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MILLIMETERS							
		TOLERANCES							
		DECIMALS X.XX + 0.25 X.XXX + 0.13		FRACTIONS + 0.4		ANGLES + 2			
		SIZE D		SCALE 1:1		CODE ID NO. CodeID		SHEET 1 OF 6	

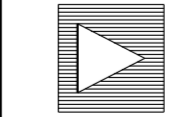
REAL-TIME ETHERNET MULTIPROTOCOL (REM) SWITCH

FIDO5210



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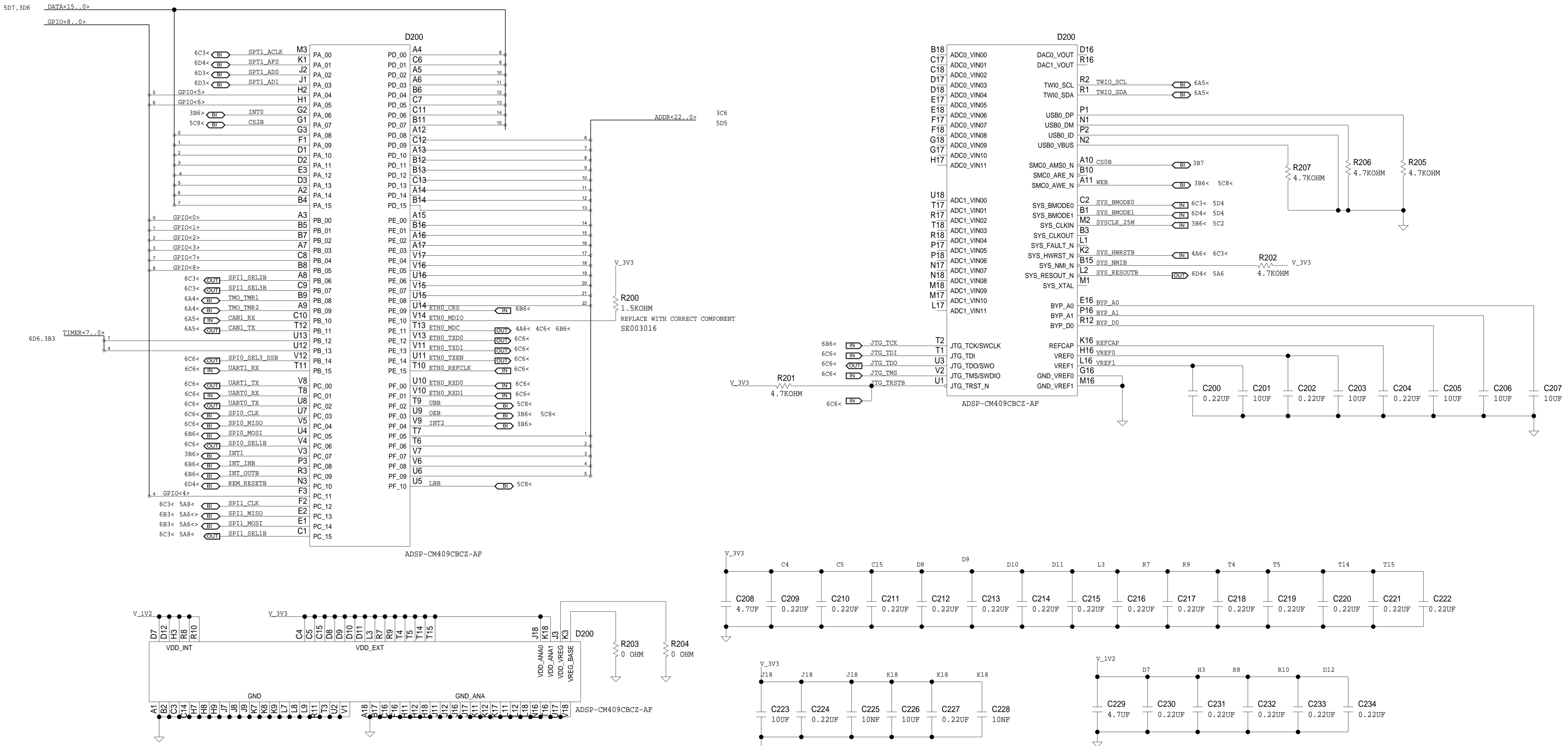


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SCHEMATIC			
HW TYPE : Package Design			
Product(s): ADIN2299			
<PRODUCT_1>			
DESIGN_VIEW	DRAWING NO.		REV
<DESIGN_VIEW>	02_058603		A
DESIGN_ENGINEER	SIZE	SCALE	SHEET 2 OF 6
R. BERRGER	D	1:1	

ARM CORTEX M4 PROCESSOR



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

SCHEMATIC			
HW TYPE : Package Design Product(s): ADIN2299 <PRODUCT_1>			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_058603	REV A	
DESIGN ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 3 OF 6

